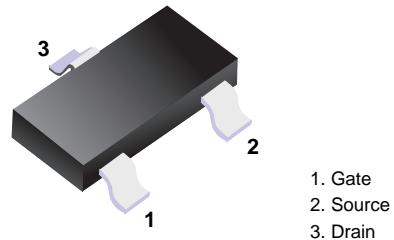


N-Channel MOSFET**■ Features**

- 0.22 A, 25 V. $R_{DS(ON)} = 4 \Omega$ @ $V_{GS} = 4.5$ V
 $R_{DS(ON)} = 5 \Omega$ @ $V_{GS} = 2.7$ V.
- Very low level gate drive requirements allowing direct operation in 3V circuits. $V_{GS(th)} < 1.5$ V.
- Gate-Source Zener for ESD ruggedness.
>6kV Human Body Model
- Replace multiple NPN digital transistors with one DMOSFET.

**■ Simplified outline(SOT-23)****■ Absolute Maximum Ratings Ta = 25°C**

| Parameter | Symbol | Rating | Unit |
|---|------------------------------------|-------------|--------|
| Drain-Source Voltage, Power Supply Voltage | V _{DSS} , V _{CC} | 25 | V |
| Gate-Source Voltage, V _{IN} | V _{GSS} , V _I | 8 | V |
| Drain/Output Current - Continuous - pulse | I _D | 0.22 0.5 | A A |
| Maximum Power Dissipation | P _D | 0.35 | W |
| Electrostatic Discharge Rating MIL-STD-883D Human Body Model (100pf / 1500 Ohm) | ESD | 6 | kV |
| Thermal Resistance, Junction-to- Ambient | R _{θJA} | 357 | °C/W |
| Operating and Storage Junction Temperature Range | T _J , T _{stg} | -55 to +150 | °C |

■ Inverter Electrical Characteristics TA = 25°C unless otherwise noted

| Parameter | Symbol | Test Conditions | Min | Typ | Max | Units |
|-----------------------------------|----------------------|--|-----|-----|-----|-------|
| Zero Input Voltage Output Current | I _O (off) | V _{CC} = 20 V, V _I = 0 V | | | 1.0 | μA |
| Input Voltage | V _I (off) | V _{CC} = 5 V, I _O = 10 μA | | | 0.5 | V |
| | V _I (on) | V _O = 0.3 V, I _O = 5 mA | 1.0 | | | V |
| Output to Ground Resistance | R _O (on) | V _I = 2.7 V, I _O = 0.2 A | | | 5.0 | Ω |

■ Electrical Characteristics $T_a = 25^\circ\text{C}$ unless otherwise noted

| Parameter | Symbol | Testconditons | Min | Typ | Max | Unit |
|---|-----------------------------|--|------|------|------|----------------------------|
| Drain-Source Breakdown Voltage | V_{DSS} | $V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$ | 25 | | | V |
| Breakdown Voltage Temp. Coefficient | $\Delta V_{DSS}/\Delta T_J$ | $I_D = 250 \mu\text{A}$, Referenced to 25°C | | 25 | | $\text{mV}/^\circ\text{C}$ |
| Zero Gate Voltage Drain Current | I_{DSS} | $V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$ | | | 1 | μA |
| | | $V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55^\circ\text{C}$ | | | 10 | μA |
| Gate-Body Leakage Current,Forward | I_{GSSF} | $V_{GS} = 8 \text{ V}, V_{DS} = 0 \text{ V}$ | | | 100 | nA |
| Gate-Body Leakage Current,Reverse | I_{GSSR} | $V_{GS} = -8 \text{ V}, V_{DS} = 0 \text{ V}$ | | | -100 | nA |
| Gate Threshold Voltage (Note) | $V_{GS(th)}$ | $V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$ | 0.65 | 0.8 | 1.5 | V |
| Gate Threshold Voltage Temp. Coefficient (Note) | $\Delta V_{GS(th)}/T_J$ | $I_D = 250 \mu\text{A}$, Referenced to 25°C | | -2.1 | | $\text{mV}/^\circ\text{C}$ |
| Static Drain-Source On-Resistance (Note) | $R_{DS(on)}$ | $V_{GS} = 4.5 \text{ V}, I_D = 0.4 \text{ A}$ | | | 4.0 | Ω |
| | | $V_{GS} = 2.7 \text{ V}, I_D = 0.2 \text{ A}$ | | | 5.0 | |
| On-State Drain Current (Note) | $I_{D(on)}$ | $V_{GS} = 2.7 \text{ V}, V_{DS} = 5 \text{ V}$ | 0.2 | | | A |
| Forward Transconductance | g_{FS} | $V_{DS} = 5 \text{ V}, I_D = 0.4 \text{ A}$ | | 0.2 | | S |
| Input Capacitance | C_{iss} | $V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1.0 \text{ MHz}$ | | 9.5 | | pF |
| Output Capacitance | C_{oss} | | | 6.0 | | |
| Reverse Transfer Capacitance | C_{rss} | | | 1.3 | | |
| Turn-On Delay Time (Note) | $t_{d(on)}$ | $V_{DD} = 6 \text{ V}, I_D = 0.5 \text{ A}, V_{GS} = 4.5 \text{ V}, R_{GEN} = 50 \Omega$ | | 3.2 | 8 | ns |
| Turn-On Rise Time (Note) | t_r | | | 6 | 15 | |
| Turn-Off Delay Time (Note) | $t_{d(off)}$ | | | 3.5 | 8 | |
| Turn-Off Fall Time (Note) | t_f | | | 3.5 | 8 | |
| Total Gate Charge (Note) | Q_g | $V_{DS} = 5 \text{ V}, I_D = 0.2 \text{ A}, V_{GS} = 4.5 \text{ V}$ | | 0.49 | 0.7 | nC |
| Gate-Source Charge (Note) | Q_{gs} | | | 0.22 | | |
| Gate-Drain Charge (Note) | Q_{gd} | | | 0.07 | | |
| Maximum Continuous Drain-Source Diode Forward Current | I_s | | | | 0.29 | A |
| Drain-Source Diode ForwardVoltage (Note) | V_{SD} | $V_{GS} = 0 \text{ V}, I_s = 0.29 \text{ A}$ | | | 1.2 | V |

Note: Pulse Test: Pulse Width $\leqslant 300 \mu\text{s}$, Duty Cycle $\leqslant 2.0\%$.

■ Typical Characteristics

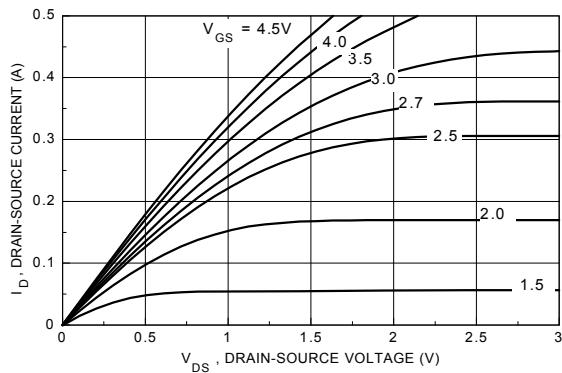


Figure 1. On-Region Characteristics.

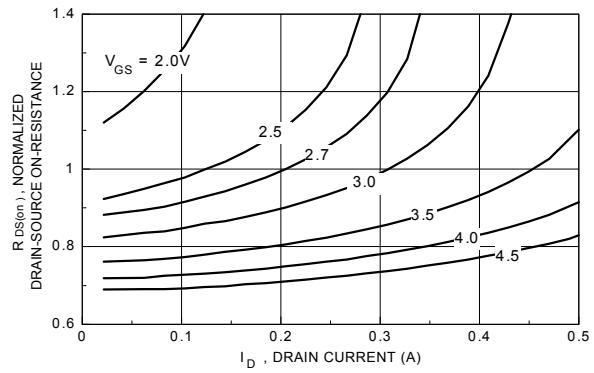


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

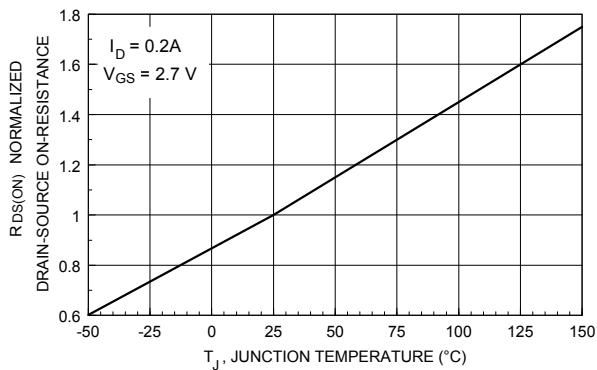


Figure 3. On-Resistance Variation with Temperature.

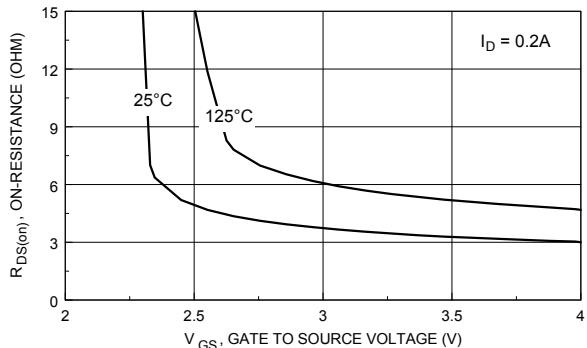


Figure 4. On Resistance Variation with Gate-To-Source Voltage.

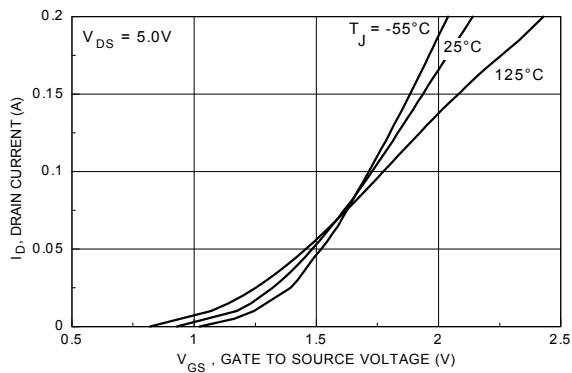


Figure 5. Transfer Characteristics.

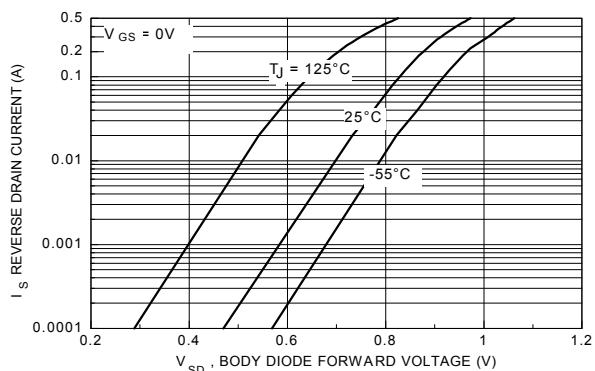


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

■ Typical Characteristics

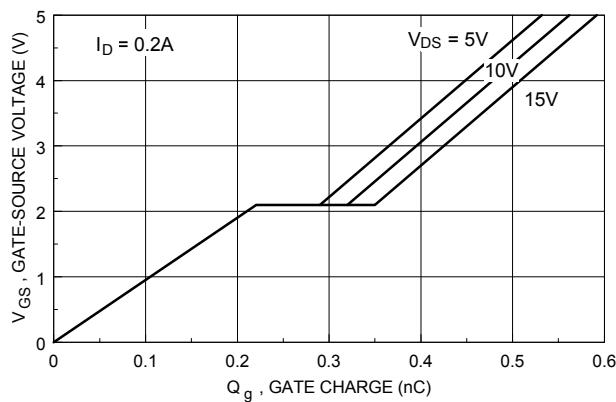


Figure 7. Gate Charge Characteristics.

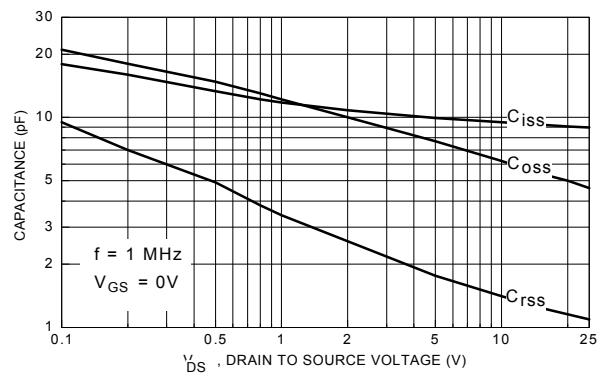


Figure 8. Capacitance Characteristics.

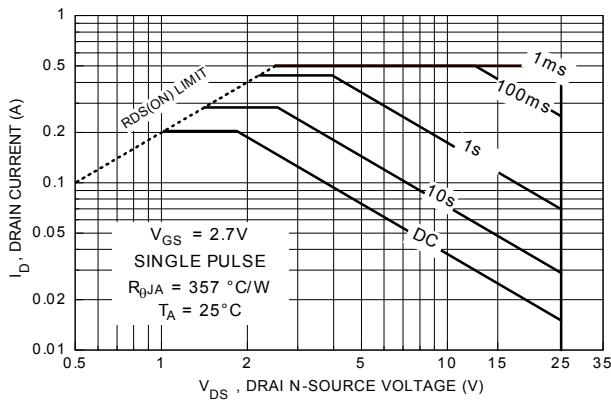


Figure 9. Maximum Safe Operating Area.

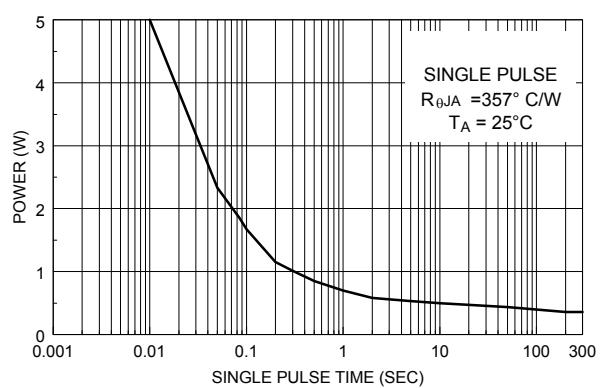


Figure 10 . Single Pulse Maximum Power Dissipation.

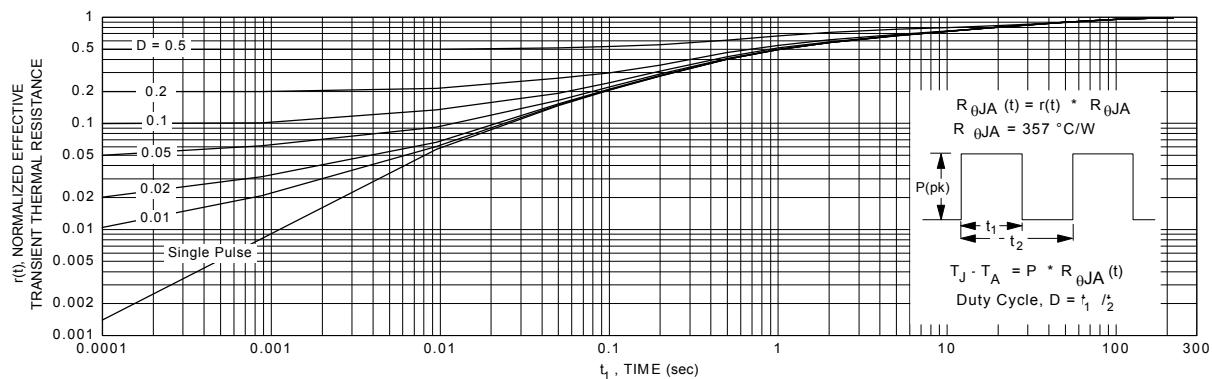
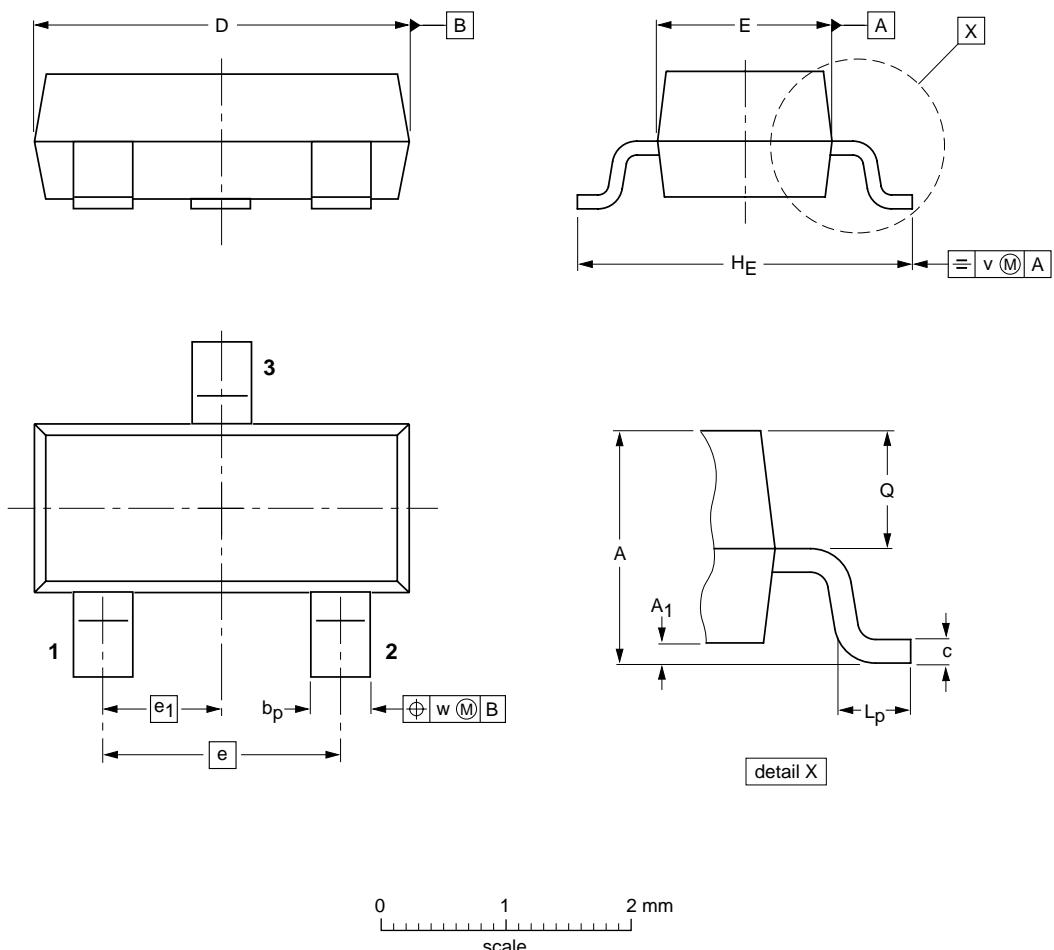


Figure 11 . Transient Thermal Response Curve .

■ SOT-23



DIMENSIONS (mm are the original dimensions)

| UNIT | A | A_1 max. | b_p | c | D | E | e | e_1 | H_E | L_p | Q | v | w |
|------|------------|---------------|--------------|--------------|------------|------------|-----|-------|------------|--------------|--------------|-----|-----|
| mm | 1.1 0.9 | 0.1 | 0.48 0.38 | 0.15 0.09 | 3.0 2.8 | 1.4 1.2 | 1.9 | 0.95 | 2.5 2.1 | 0.45 0.15 | 0.55 0.45 | 0.2 | 0.1 |